L Number	Hits	Search Text	DB	Time stamp
1	2	(memory adj die) and (controller adj die) and	USPAT	2004/05/06
		438/\$.ccls.		18:03
2	13	(memory adj die) and controller and 438/\$.ccls.	USPAT	2004/05/06 18:01
3	132	memory and controller and (circuit adj board )and 438/\$.ccls.	USPAT	2004/05/06
4	5	(memory and controller and (circuit adj board )and	USPAT	2004/05/06
5	3	438/\$.ccls.) and (cutting and laminating) (memory adj die) and (controller adj die) and	USPAT;	18:02 2004/05/06
		438/\$.ccls.	US-PGPUB; EPO; JPO	17:59
6	3	(memory adj die) and (controller adj die) and 438/\$.ccls.	USPAT; US-PGPUB;	2004/05/06 18:00
		· · · · · · · · · · · · · · · · · · ·	EPO; JPO	
7	19	(memory adj die) and controller and 438/\$.ccls.	USPAT; US-PGPUB; EPO; JPO;	2004/05/06
8	5	(memory and controller and (circuit adj board )and	DERWENT USPAT;	2004/05/06
•	,	438/\$.ccls.) and (cutting and laminating)	US-PGPUB; EPO; JPO	18:02
9	10	(memory adj die) and (controller adj die) and 257/\$.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/05/06 18:04
10	9540	"10" and cutting and laminating	USPAT	2004/05/06 18:04
11	0	((memory adj die) and (controller adj die) and 257/\$.ccls.) and cutting and laminating	USPAT	2004/05/06 18:05
13	5	(memory and controller and (circuit adj board )and 257/\$.ccls.) and cutting and laminating	USPAT	2004/05/06 18:06
12	738	memory and controller and (circuit adj board )and 257/\$.ccls.	USPAT; US-PGPUB;	2004/05/06 18:24
14	3612	memory and (plurality adjcontroller) and (circuit adj board )and 257/\$.ccls.	EPO; JPO USPAT; US-PGPUB; EPO; JPO	2004/05/06 18:25
15	2	memory and (plurality adj controller) and (circuit adj board )and 257/\$.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/05/06 18:27
16	1267	memory and (plurality adjcontroller) and (circuit adj board )and 365/\$.ccls.	USPAT; US-PGPUB;	2004/05/06 18:25
17	1	memory and (plurality adj controller) and (circuit adj	EPO; JPO USPAT;	2004/05/06
		board )and 365/\$.ccls.	US-PGPUB; EPO; JPO	18:26
18	9	memory and (plurality adj2 controller) and (circuit adj board )and 365/\$.ccls.	USPAT; US-PGPUB; EPO; JPO	2004/05/06 18:26
19	11	memory and (plurality adj2 controller) and (circuit adj board )and 257/\$.ccls.	USPAT; US-PGPUB;	2004/05/06 18:28
		·	EPO; JPO	

3 memory and (plurality adj2 controller) and (circuit USPAT; 2004/05/06 adj board )and 438/\$.ccls. US-PGPUB; EPO; JPO